

What is claimed is:

1. A fuse bank of a semiconductor memory device comprising:

a first laser fuse which includes a first laser fusing region which is disposed in a first direction, a first connecting line region which is disposed to be bent in a second direction, and

a second connecting line region which is disposed to be bent in a third direction; and

a second laser fuse which includes a second laser fusing region which is disposed in the first direction, a third connecting line region which is disposed to be bent in the second direction, and a fourth connecting line region which is disposed to be bent in the third direction,

wherein the first laser fuse and the second laser fuse are disposed adjacently with a space of a predetermined distance there between,

the first laser fusing region and the second laser fusing region form a laser fusing region of the fuse bank, and

the first laser fuse and the second laser fuse are disposed on a plane.

2. The fuse bank of claim 1, wherein the laser fusing region has a parallelogram shape.

3. The fuse bank of claim 1, wherein the first direction is perpendicular to the second direction and the third direction.

4. The fuse bank of claim 3, wherein the second direction is opposite to the third direction.

5. A fuse bank of a semiconductor memory device comprising:

a first laser fuse group which has multiple laser fuses arranged in a first direction with a space of a predetermined distance there between; and

a second laser fuse group that has multiple laser fuses arranged in the first direction with a space of a predetermined distance there between,

wherein the first laser fuse group and the second laser fuse group each include a laser fusing region which is disposed in the first direction, a first connecting line region which is disposed to be bent in a second direction, and a second connecting line region which is disposed to be bent in a third direction, and

the first laser fuse and the second laser fuse are adjacently disposed on a plane.

6. The fuse bank of claim 5, wherein the laser fusing region has a parallelogram shape.

7. The fuse bank of claim 5, wherein the first direction is perpendicular to the second direction and the third direction.

8. The fuse bank of claim 7, wherein the second direction is opposite to the third direction.

9. The fuse bank of claim 5, wherein the first laser fuse group and the second laser fuse group are disposed repeatedly.

10. A fuse bank of a semiconductor memory device comprising:
a first laser fuse group which has multiple laser fuses arranged in a first direction with a space of a predetermined distance there between; and
a second laser fuse group that has multiple laser fuses arranged in the first direction with a space of a predetermined distance there between,
wherein the first laser fuse group and the second laser fuse group each include a laser fusing region which is disposed in the first direction, a first connecting line region which is disposed to be bent in a second direction, and a second connecting line region which is disposed to be bent in a third direction,
the first laser fuse group and the second laser fuse group are disposed adjacently,
the first laser fuse group and the second laser fuse group are disposed to be symmetrical about the direction perpendicular to the first direction, and
the first laser fuse group and the second laser fuse group are disposed on a plane.

11. The fuse bank of claim 10, wherein the laser fusing region has a parallelogram shape.

12. The fuse bank of claim 10, wherein the first direction is perpendicular to the second direction and the third direction.

13. The fuse bank of claim 12, wherein the second direction is opposite to the third direction.

14. The fuse bank of claim 10, wherein the first laser fuse group and the second
5 laser fuse group are disposed repeatedly.

15. A fuse bank, comprising:
a fuse region formed from a first fuse region and a second fuse region arranged
parallel to each other, each with a first end and a second end; and
10 connecting lines connected to each of the first and second fuse regions, such that each
of the first and second fuse regions has a connecting line on each end, wherein connecting
lines on the first end are perpendicular to the first and second fuse regions in a first direction
and connecting lines on the second end are perpendicular to the first and second fuse regions
in a second direction.

15 16. The fuse bank of claim 15, the first and second fuse regions being offset from
each other a predetermined distance.

17. The fuse bank of claim 15, connecting lines at each end of the fuse region
20 being offset from each other a predetermined distance.

18. A fuse bank, comprising:
at least two fuses, each fuse comprising:
a plurality of fuse regions, each having a first end and a second end, arranged
25 parallel to each other and offset from each other a predetermined distance; and
a plurality of connecting lines, one disposed at the first and second ends of
each of the plurality of fuse regions, wherein the plurality of connecting lines are
perpendicular to the plurality of fuse regions.

30 19. The fuse bank of claim 18, wherein the connecting lines disposed at the first
end of a first fuse and a second fuse arranged in a first direction and the connecting lines
disposed at the second end of the first fuse and the second fuse arranged in a second
direction.

20. The fuse bank of claim 18, wherein the connecting lines at the first end of a first fuse and the second end of a second fuse arranged in a first direction and the connecting lines at the second end of the first fuse and the first end of the second fuse arranged in a second direction.

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